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Applicants) Richard S. NORMAN et al.	7				
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Docket Number (Optional) Application Number 13692 09/870.841 INFORMATION DISCLOSURE OF ATION Applicant(s) Richard S. NORMAN et al. (Use several sheeps in ecessary) Group Art Unit Filing Date TET 2 3 2003 June 1, 2001 2661 **U.S. PATENT DOCUMENTS** EXAMINER FILING DATE SUBCLASS DOCUMENT NUMBER DATE NAME CLASS INITIAL IF APPROPRIATE U.S. 5,790,539 08-04-1998 Chao et al. 11-03-1998 Varma et al. U.S. 5,831,980 05-30-2000 Ayandeh U.S. 6,069,895 Technology Center 2600 FOREIGN PATENT DOCUMENTS CLASS SUBCLASS REF DOCUMENT NUMBER COUNTRY DATE YES NO 10-14-1987 **EUROPE** EP 0 241 152 A2 OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) NOTANI H ET AL: "An 8*8 ATM switch LSI with shared multi-buffer architecture" PROCEEDINGS OF THE SYMPOSIUM ON VLSI CIRCUITS. SEATTLE, JUNE 4-6, 1992, SYMPOSIUM ON VLSI CIRCUITS, NEW YORK, IEEE, US, 4 June 1992 (1992-06-04), pages 74-75, XP010064987 International Search Report PCT/CA02/00810, August 1, 2003 DATE CONSIDERED EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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	EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								

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	INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				Applicant(s) Richard S. Norman et al.						
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